



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(ON)}	I _D T _A = +25°C
-50V	6Ω @ $V_{GS} = -4 V$	-200mA
	$8\Omega @ V_{GS} = -2.5V$	-160mA

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays

Features and Benefits

- Low On-Resistance
- ESD Protected Gate
- Low Input/Output Leakage
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

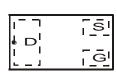
- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 (4)
- Terminal Connections: See Diagram
- Weight: 0.001 grams (Approximate)



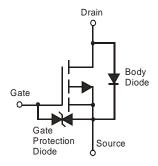




Bottom View



Top View Internal Schematic



Equivalent Circuit

Ordering Information (Note 4)

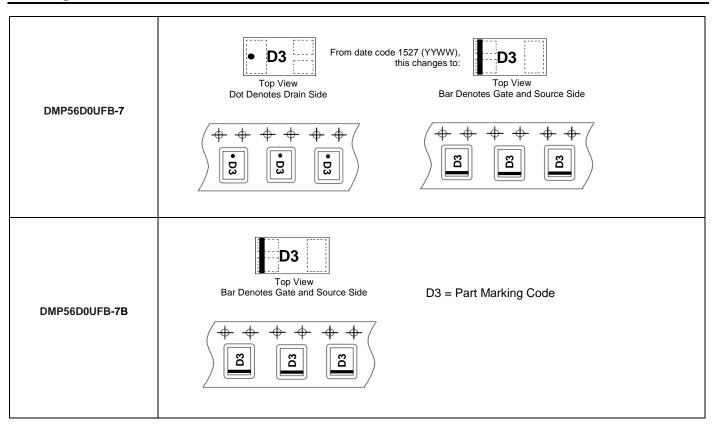
Part Number	Case	Packaging
DMP56D0UFB-7	X1-DFN1006-3	3,000/Tape & Reel
DMP56D0UFB-7B	X1-DFN1006-3	10,000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html



Marking Information



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	-50	V	
Gate-Source Voltage			V _{GSS}	±8	V
Drain Current (Note 5)	Steady	$T_A = +25$ °C	Ι _D	-200	mA
Pulsed Drain Current (Note 6)			I _{DM}	-700	mA

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_{D}	425	mW
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{0JA}	275	°C/W
Operating and Storage Temperature Range	$T_{J_1}T_{STG}$	-55 to +150	°C

Notes: 5. Device mounted on FR-4 PCB. t ≤5 sec.

6. Pulse width ≤10 μ S, Duty Cycle ≤1%.



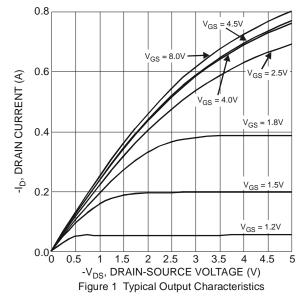
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

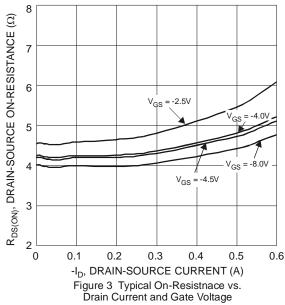
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	-50	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	-	-	-10	μΑ	$V_{DS} = -50V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±1	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	-0.5		-1.2	٧	$V_{DS} = V_{GS}, I_D = -250 \mu A$
Static Drain-Source On-Resistance	D		4.6	6 8	Ω	$V_{GS} = -4.0V, I_D = -100mA$
Static Diani-Source On-Resistance	R _{DS (ON)}		6			$V_{GS} = -2.5V, I_D = -80mA$
Forward Transfer Admittance	Y _{fs}	100	_	_	mS	$V_{DS} = -5V, I_{D} = -100mA$
Diode Forward Voltage (Note 7)	V_{SD}	_	_	-1.2	V	$V_{GS} = 0V, I_S = -100mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	50.54	_	рF	V _{DS} = -25V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	Coss	_	3.49	_	pF	
Reverse Transfer Capacitance	C _{rss}	-	2.42		pF	
Gate Resistance	R _G	1	201		Ω	$V_{DS} = 0V, V_{GS} = 0V,$ f = 1.0MHz
Total Gate Charge V _{GS} = 4.5V	Q_g		0.58		nC	V _{GS} = -4V, V _{DS} = -25V, I _D = -100mA
Gate-Source Charge	Q_{gs}	_	0.09	_	nC	
Gate-Drain Charge	Q_{gd}	_	0.14	_	nC	
Turn-On Delay Time	t _{D(on)}	_	4.46	_	nS	$V_{DD} = -30V, I_{D} = -0.27A,$ $V_{GEN} = -4V, R_{GEN} = 6\Omega$
Turn-On Rise Time	t _r	_	6.63	_	nS	
Turn-Off Delay Time	t _{D(off)}	_	21.9	_	nS	
Turn-Off Fall Time	t _f	_	15.0	_	nS	

Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to production testing.







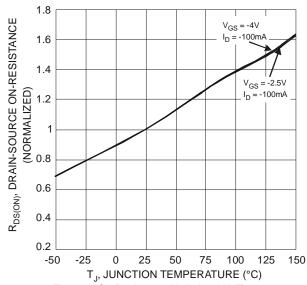
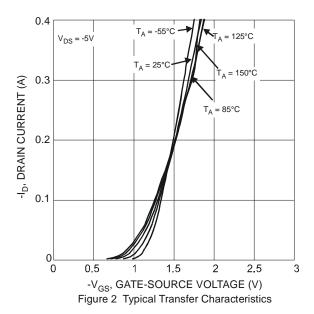
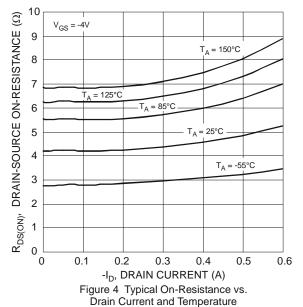


Figure 5 On-Resistance Variation with Temperature





10 $R_{\mathsf{DS}(\mathsf{ON})}$, DRAIN-SOURCE ON-RESISTANCE (Ω) 9 8 $V_{GS} = -2.5V,$ 7 I_D = -100mA 6 V_{GS} = -4V, I_D = -100mA 2 0 -25 25 75 100 125 150 -50 50 T_{.I}, JUNCTION TEMPERATURE (°C)



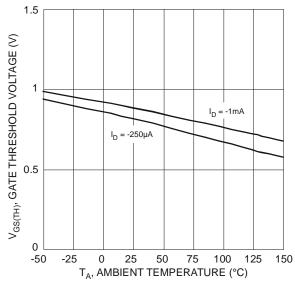
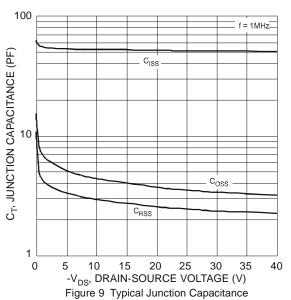
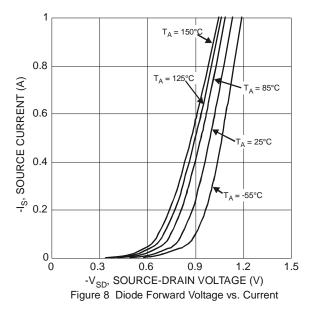


Figure 7 Gate Threshold Variation vs. Ambient Temperature

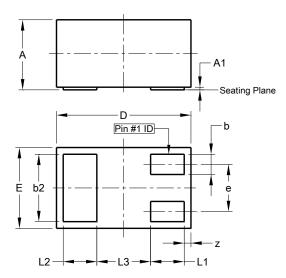






Package Outline Dimensions

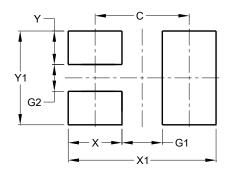
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X1-DFN1006-3				
Dim	Min	Max	Тур	
A	0.47	0.53	0.50	
A1	0.00	0.05	0.03	
b	0.10	0.20	0.15	
b2	0.45	0.55	0.50	
D	0.95	1.075	1.00	
Е	0.55	0.675	0.60	
e	ı	-	0.35	
ľ	0.20	0.30	0.25	
L2	0.20	0.30	0.25	
L3	-	-	0.40	
Z	0.02	0.08	0.05	
All Dimensions in mm				

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	0.70		
G1	0.30		
G2	0.20		
Х	0.40		
X1	1.10		
Y	0.25		
Y1	0.70		

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