

SP339B

FEBRUARY 2018

GENERAL DESCRIPTION

The SP339B is an advanced multiprotocol transceiver supporting RS-232, RS-485, and RS-422 serial standards in a 40 pin QFN package. Integrated cable termination and four configuration modes allow all three protocols to be used interchangeably over a single cable or connector with no additional switching components. Full operation requires only four external charge pump capacitors.

The RS-485/422 modes feature one driver and one receiver (1TX/1RX) in both half and full duplex configurations. The RS-232 mode (3TX/5RX) provides full support of all eight signals commonly used with the DB9 RS-232 connector. A dedicated diagnostic loopback mode is also provided.

The high speed drivers operate up to 20Mbps in RS-485/422 modes, and up to 1Mbps in RS-232 mode. All drivers can be slew limited to 250kbps in any mode to minimize electromagnetic interference (EMI).

All transmitter outputs and receiver inputs feature robust electrostatic discharge (ESD) protection to

±15kV IEC-61000-4-2 Air Gap, ±8kV IEC-61000-4-2 Contact, and ±15kV Human Body Model (HBM). Each receiver output has full fail-safe protection to avoid system lockup, oscillation, or indeterminate states by defaulting to logic-high output level when the inputs are open, shorted, or terminated but undriven. No external biasing resistors are required.

The RS-232 receiver inputs include a $5k\Omega$ pull-down to ground. The RS-485/422 receiver inputs are high impedance (>96k Ω when termination is disabled), allowing up to 256 devices on a single communication bus (1/8th unit load).

The SP339B operates from a single power supply, either 3.3V or 5V, with low idle current (2mA typical in all modes). The shutdown mode consumes less than 10μ A for low power standby operation.

FEATURES

- Rx enabled during Tx short circuit condition
- Pin selectable Cable Termination
- No external resistors required for RS-485/422 termination and biasing
- 3.3V or 5V Single Supply Operation
- Robust ESD Protection on bus pins
 - ±15kV IEC 61000-4-2 (Air Gap)
 - ± 8kV IEC 61000-4-2 (Contact)
 - ±15kV Human Body Model (HBM)
- Max Data Rate of 20Mbps in RS-485/422 Modes and up to 1Mbps in RS-232 Modes
- Pin selectable 250kbps Slew Limiting
- 3 Drivers, 5 Receivers RS-232/V.28
- 1 Driver, 1 Receiver RS-485/422
 - Full and Half Duplex Configuration
 - 1/8th Unit Load, up to 256 receivers on bus
- RS-485/422 Enhanced Failsafe for open, shorted, or terminated but idle inputs
- Space saving 6mm x 6mm QFN-40 Package
- Pin compatible with SP339E and SP338E

TYPICAL APPLICATIONS

- Dual Protocol Serial Ports (RS-232 or RS-485/422)
- Industrial Computers
- Industrial and Process Control Equipment
- Point-Of-Sale Equipment
- Networking Equipment
- HVAC Controls Equipment
- Building Security and Automation Equipment

ORDERING INFORMATION⁽¹⁾

PART NUMBER	OPERATING TEMPERATURE RANGE	LEAD-FREE	PACKAGE	PACKAGING METHOD
SP339BER1-L	-40°C to +85°C	Yes ⁽²⁾	40-pin QFN	Tray
SP339BER1-L/TR	-40°C to +85°C	165.7	40-pin QFN	Tape and Reel

NOTE:

1. Refer to.www.exar.com/SP339B for most up-to-date Ordering Information.

2. Visit www.exar.com for additional information on Environmental Rating.

ABSOLUTE MAXIMUM RATINGS

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections to the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability and cause permanent damage to the device.

Supply Voltage V _{CC}	-0.3V to +6.0V			
Receiver Input Voltage (from Ground)	±18V			
Driver Output Voltage (from Ground)	±18V			
Short Circuit Duration, TX out to Ground	Continuous			
Voltage at TTL Input Pins	-0.3V to (V _{CC} + 0.5V)			
Storage Temperature Range	-65°C to +150°C			
Lead Temperature (soldering, 10s)	+300°C			
Power Dissipation 40-pin QFN (derate 17mW/°C above +70°C)	500mW			
ESD Ratings				
HBM - Human Body Model (Tx Output & Rx Input pins, R1-R9)	±15kV			
HBM - Human Body Model (All other pins)	±4kV			
IEC 61000-4-2 Airgap Discharge (Tx Output & Rx Input pins, R1-R9)	±15kV			
IEC 61000-4-2 Contact Discharge (Tx Output & Rx Input pins, R1-R9)	±8kV			

CAUTION:

ESD (ElectroStatic Discharge) sensitive device. Permanent damage may occur on unconnected devices subject to high energy electrostatic fields. Unused devices must be stored in conductive foam or shunts. Personnel should be properly grounded prior to handling this device. The protective foam should be discharged to the destination socket before devices are removed.





RS-232/RS-485/RS-422 TRANSCEIVER WITH INTERNAL TERMINATION

PIN DESCRIPTIONS BY MODE (MODE1, MODE0)

Pin	Name	00, Figure 1 01, Figure 2		10, Figure 3	11 , Figure 4			
1	L1	R1 0	utput	1	1			
2	L2	R2 0	utput	R1 Output	R1 Output			
3	L3	T1 li	nput	T1 Input	T1 Input			
4	L4	T2 I	nput					
5	L6	R3 0	utput	1	1			
6	L7	T3 li	nput					
7	L8	R4 0	utput	1	1			
8	L9	R5 0	utput	1 1				
9	VCC	V _{CC}						
10	GND	Ground						
11	SLEW		SLEW = V	V _{CC} enables 250kbps slew limiting				
12	DIR1				T1 Enable			
13	N/C	Tł	nis pin is not used and	is not connected internal	lly			
14	MODE0	0	1	0	1			
15	MODE1	0	0	1	1			
16	N/C	This pin is not used and is not connected internally						
17	TERM	Enables RS-485/422 receiver termination						
18	N/C	Tł	nis pin is not used and	is not connected internal	lly			
19	ENABLE	ENAE	BLE = V _{CC} for operation	n, ENABLE = 0V for shut	down			
20	VCC		V	′сс				

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RS-232/RS-485/RS-422 TRANSCEIVER WITH INTERNAL TERMINATION

PIN DESCRIPTIONS BY MODE (MODE1, MODE0)

Pin	Name	00, Figure 1	01 , Figure 2	10, Figure 3	11 , Figure 4					
21	R9		R5 Input							
22	R8									
23	GND		G	round						
24	R7		T3 Output							
25	R6		R3 Input							
26	GND		G	round						
27	R4		T2 Output		R1 Input B					
28	R3		T1 Output		R1 Input A					
29	GND	Ground								
30	R2		R2 Input	R1 Input A, T1 Out A	T1 Out A					
31	R1		R1 Input	R1 Input B, T1 Out B	T1 Out B					
32	VCC	V _{CC} -	V_{CC} - 1.0µF to ground recommended for supply decoupling							
33	VSS	V_{SS} - Charge pump negative supply, $0.1 \mu F$ from ground								
34	C2-		C ₂₊ - Charge pum	p cap 2 negative lead						
35	C1-		C ₁₋ - Charge pum	p cap 1 negative lead						
36	GND		G	round						
37	C1+		C ₁₊ - Charge pump c	ap 1 positive lead, $0.1 \mu F$						
38	VCC			V _{CC}						
39	C2+		C ₂₊ - Charge pump c	ap 2 positive lead, $0.1 \mu F$						
40	VDD	V _E	_{DD} - Charge pump posi	itive supply, $0.1\mu F$ to grour	nd					



RS-232/RS-485/RS-422 TRANSCEIVER WITH INTERNAL TERMINATION

SUGGESTED DB9 CONNECTOR PINOUT

DB9 Pin	RS-232	RS-485/422 Full Duplex	RS-485 Half Duplex
1	DCD	TX-	Data-
2	RXD	TX+	Data+
3	TXD	RX+	
4	DTR	RX-	
5		Ground	
6	DSR		
7	RTS		
8	CTS		
9	RI		



ELECTRICAL CHARACTERISTICS

UNLESS OTHERWISE NOTED:

 V_{CC} = +3.3V ±5% or +5.0V ±5%, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX}. Typical values are at V_{CC} = 3.3V, T_A = +25°C.

SYMBOL	PARAMETERS	Min.	Түр.	MAX.	UNITS	Conditions
DC CHARAG	CTERISTICS					
I _{CC}	Supply Current (RS-232)		2	8	mA	No load, idle inputs
I _{CC}	Supply Current (RS-485)		2	8	mA	No load, idle inputs
I _{CC}	Vcc Shutdown Current		1	10	μA	ENABLE = 0V
TRANSMITT	ER and LOGIC INPUT PINS: Pins 3, 4,	6, 11, 12	, 14, 15,	17-19		
V _{IH}	Logic Input Voltage High	2.0			V	V _{CC} = 3.3V
V _{IH}	Logic Input Voltage High	2.4			V	V _{CC} = 5.0V
V _{IL}	Logic Input Voltage Low			0.8	V	
I	Logic Input Leakage Current Low			1	μA	Input Low (V _{IN} = 0V)
Ι _{ΙΗ}	Logic Input Leakage Current High			1	μA	Input High (V _{IN} = V _{CC}), pins 3, 4 and 6
I _{PD}	Logic Input Pull-down Current			50	μA	Input High (V _{IN} = V _{CC}), pins 11, 12, 14, 15, 17-19
V _{HYS}	Logic Input Hysteresis		200		mV	
RECEIVER	OUTPUTS: Pins 1, 2, 5, 7, 8					
V _{OH}	Receiver Output Voltage High	V _{CC} -0.6			V	I _{OUT} = -1.5mA
V _{OL}	Receiver Output Voltage Low			0.4	V	I _{OUT} = 2.5mA
I _{OSS}	Receiver Output Short Circuit Current		±20	±60	mA	$0 \leq V_O \leq V_{CC}$
I _{OZ}	Receiver Output Leakage Current		±0.1	±1	μA	$0 \le V_O \le V_{CC,}$ Receivers disabled



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RS-232/RS-485/RS-422 TRANSCEIVER WITH INTERNAL TERMINATION

ELECTRICAL CHARACTERISTICS (Continued)

UNLESS OTHERWISE NOTED: V_{CC} = +3.3V ±5% or +5.0V ±5%, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX}. Typical values are at V_{CC} = 3.3V, T_A = +25°C.

Symbol	PARAMETERS	Min.	Typ.	Max.	Units	CONDITIONS
SINGLE-EN	DED RECEIVER INPUTS (RS-232)					
V _{IN}	Input Voltage Range	-15		+15	V	
V _{IL}	Input Threshold Low	0.6	1.2		V	V _{CC} = 3.3V
۷L		0.8	1.5		V	V _{CC} = 5.0V
V _{IH}	Input Threshold High		1.5	2.0	V	V _{CC} = 3.3V
۹H			1.8	2.4	V	V _{CC} = 5.0V
V _{HYS}	Input Hysteresis		0.3		V	
R _{IN}	Input Resistance	3	5	7	kΩ	$-15V \le V_{IN} \le +15V$
SINGLE-EN	DED DRIVER OUTPUTS (RS-232)		•	1		
Vo	Output Voltage Swing	±5.0	±5.5		V	Output loaded with $3k\Omega$ to Gnd
•0				±7.0	V	No load output
I _{SC}	Short Circuit Current			±60	mA	V _O = 0V
R _{OFF}	Power Off Impedance	300	10M		Ω	$V_{CC} = 0V, V_{O} = \pm 2V$



ELECTRICAL CHARACTERISTICS (Continued)

UNLESS OTHERWISE NOTED:

 V_{CC} = +3.3V ±5% or +5.0V ±5%, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX}. Typical values are at V_{CC} = 3.3V, T_A = +25°C.

SYMBOL	PARAMETERS MIN. TYP. MAX. UNITS		CONDITIONS			
DIFFERENT	IAL RECEIVER INPUTS (RS-485 / RS-4	22)				
R _{IN}	Receiver Input Resistance	96			kΩ	TERM = 0V, -7V $\leq V_{IN} \leq +12V$
V_{TH}	Receiver Differential Threshold Voltage	-200	-125	-50	mV	
ΔV_{TH}	Receiver Input Hysteresis		25		mV	V _{CM} = 0V
I _{IN}	Receiver Input Current			125	μA	V _{IN} = +12V
'IN				-100	μΑ	V _{IN} = -7V
R _{TERM}	Termination Resistance	100	120	155	Ω	TERM = V _{CC} , Figure 5 -7V \leq V _{CM} \leq +12V
R _{TERM}	Termination Resistance	100	120	140	Ω	TERM = V _{CC} , Figure 5 V _{CM} = 0V
DIFFERENT	IAL DRIVER OUTPUTS (RS-485 / RS-42	2 2)		V _{CC}	V	R _I = 100Ω (RS-422), Figure 6
	Differential Driver Output	1.5		V _{CC}	V	$R_L = 54\Omega$ (RS-485), Figure 6
V _{OD}		1.5		V _{CC}	V	$-7V \le V_{CM} \le +12V$, Figure 7
				V _{CC}	V	No Load
ΔV_{OD}	Change In Magnitude of Differential Output Voltage	-0.2		+0.2	V	R _L = 54Ω or 100Ω, Figure 6
V _{CM}	Driver Common Mode Output Voltage			3	V	$R_L = 54\Omega$ or 100 Ω , Figure 6
ΔV_{CM}	Change In Magnitude of Common Mode Output Voltage			0.2	V	$R_L = 54\Omega$ or 100Ω, Figure 6
I _{OSD}	Driver Output Short Circuit Current	-250		250	mA	-7V \leq V _O \leq +12V, Figure 8
la.				100	μA	DIR1 = 0V in Mode 11, or ENABLE = 0V, V _O = +12V, V _{CC} = 0V or 5.25V
Ι _Ο	Driver Output Leakage Current	-100			μA	DIR1 = 0V in Mode 11, or ENABLE = 0V, V _O = -7V, V _{CC} = 0V or 5.25V



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TIMING CHARACTERISTICS

UNLESS OTHERWISE NOTED:

 V_{CC} = +3.3V ±5% or +5.0V ±5%, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX}. Typical values are at V_{CC} = 3.3V, T_A = +25°C.

SYMBOL	PARAMETERS	Min.	Typ.	Max.	UNITS	CONDITIONS	
ALL MODES							
t _{ENABLE}	Enable from Shutdown		1000		ns		
t _{SHUTDOWN}	Enable to Shutdown		1000		ns		
RS-232, DAT	A RATE = 250kbps (SLEW = Vcc), ON		SMITTER	SWITC	HING		
	Maximum Data Rate	250			kbps	R _L = 3kΩ, C _L = 1000pF	
t _{RHL} , t _{RLH}	Receiver Propagation Delay		100		ns	C _I = 150pF, Figure 9	
t _{RHL} -t _{RLH}	Receiver Propagation Delay Skew			100	ns	$C_{L} = 150 \text{ pr}, \text{ Figure 9}$	
t _{DHL} , t _{DLH}	Driver Propagation Delay		1400		ns	R _L = 3kΩ, C _L = 2500pF,	
t _{DHL} -t _{DLH}	Driver Propagation Delay Skew			600	ns	Figure 10	
	I					I	
t _{SHL,} t _{SLH}	Transition Region Slew Rate from +3.0V to -3.0V or -3.0V to +3.0V	4		30	V/µs	V_{CC} = 3.3V, R_L = 3k Ω to 7k Ω , C _L = 150pF to 2500pF, Figure 10	
t _{SHL,} t _{SLH}	Transition Region Slew Rate from +3.0V to -3.0V or -3.0V to +3.0V	6		30	V/μs	V_{CC} = 3.3V, R _L = 3kΩ to 7kΩ, C _L = 150pF to 2500pF, T _A = 25°C, Figure 10	
RS-232, DAT	A RATE = 1Mbps (SLEW = 0V), ONE T	RANSM		WITCHIN	IG		
	Maximum Data Rate	1			Mbps	R _L = 3kΩ, C _L = 250pF	
t _{RHL} , t _{RLH}	Receiver Propagation Delay		100		ns		
t _{RHL} -t _{RLH}	Receiver Propagation Delay Skew			100	ns	C _L = 150pF, Figure 9	
t _{DHL} , t _{DLH}	Driver Propagation Delay		300		ns	R _L = 3kΩ, C _L = 1000pF,	
t _{DHL} -t _{DLH}	Driver Propagation Delay Skew			150	ns	Figure 10	
	1	I	1	1	1	1	
t _{SHL,} t _{SLH}	Transition Region Slew Rate from +3.0V to -3.0V or -3.0V to +3.0V	15		150	V/µs	V_{CC} = 3.3V, R_L = 3k Ω to 7k Ω , C _L = 150pF to 1000pF, Figure 10	
t _{SHL,} t _{SLH}	Transition Region Slew Rate from +3.0V to -3.0V or -3.0V to +3.0V			150	V/µs	V_{CC} = 3.3V, R _L = 3kΩ to 7kΩ, C _L = 150pF to 1000pF, T _A = 25°C, Figure 10	



TIMING CHARACTERISTICS (Continued)

UNLESS OTHERWISE NOTED: V_{CC} = +3.3V ±5% or +5.0V ±5%, C1-C4 = 0.1µF; T_A = T_{MIN} to T_{MAX}. Typical values are at V_{CC} = 3.3V, T_A = +25°C.

SYMBOL	PARAMETERS	Min.	Typ.	Max.	UNITS	CONDITIONS	
RS-485/RS-42	2, DATA RATE = 250kbps (SLEW = V	′cc), ONE	TRANSI	WITTER	SWITCH	lING	
	Maximum Data Rate	250			kbps	$R_L = 54\Omega, C_L = 50pF$	
t _{RPHL} , t _{RPLH}	Receiver Propagation Delay		50	150	ns	C ₁ = 15pF, Figure 11	
t _{RPHL} -t _{RPLH}	Receiver Propagation Delay Skew			20	ns	CL - Topr, Figure T	
t _{DPHL} , t _{DPLH}	Driver Propagation Delay		500	1000	ns		
t _{DPHL} -t _{DPLH}	Driver Propagation Delay Skew			100	ns	R _L = 54Ω, C _L = 50pF, Figure 12	
t _{DR,} t _{DF}	Driver Rise and Fall Time	300	650	1200	ns		
		-					
t _{RZH} , t _{RZL}	Receiver Output Enable Time			200	ns	C _I = 15pF, Figure 13	
t _{RHZ} , t _{RLZ}	Receiver Output Disable Time			200	ns		
t _{DZH} , t _{DZL}	Driver Output Enable Time			1000	ns	R _L = 500Ω, C _L = 50pF,	
t _{DHZ} , t _{DLZ}	Driver Output Disable Time			200	ns	Figure 14	
RS-485/RS-42	2, DATA RATE = 20Mbps (SLEW = 0\ Maximum Data Rate	/), ONE T 20	RANSMI		WITCHII Mbps	NG R _L = 54Ω, C _L = 50pF	
t _{RPHL} , t _{RPLH}	Receiver Propagation Delay		50	150	ns		
t _{RPHL} -t _{RPLH}	Receiver Propagation Delay Skew			10	ns	C _L = 15pF, Figure 11	
t _{DPHL} , t _{DPLH}	Driver Propagation Delay		30	100	ns		
t _{DPHL} -t _{DPLH}	Driver Propagation Delay Skew			10	ns	$R_L = 54\Omega, C_L = 50pF,$	
t _{DR,} t _{DF}	Driver Rise and Fall Time		10	20	ns	Figure 12	
	1	1	<u>I</u>	1	1	1	
					1		
t _{RZH} , t _{RZL}	Receiver Output Enable Time			200	ns	$C_{\rm c} = 15 \text{pE}$ Figure 13	
t _{RZH} , t _{RZL}	Receiver Output Enable Time Receiver Output Disable Time			200 200	ns ns	C _L = 15pF, Figure 13	
						$C_{L} = 15$ pF, Figure 13 $R_{L} = 500$ Ω, $C_{L} = 50$ pF,	



RS-232/RS-485/RS-422 TRANSCEIVER WITH INTERNAL TERMINATION

BLOCK DIAGRAM BY MODE (MODE1, MODE0)

FIGURE 1. MODE 00 - LOOPBACK

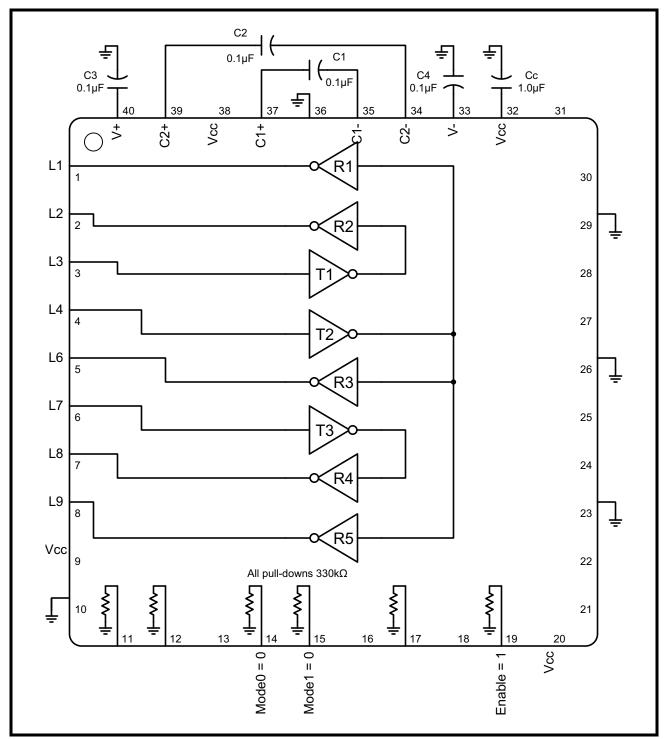
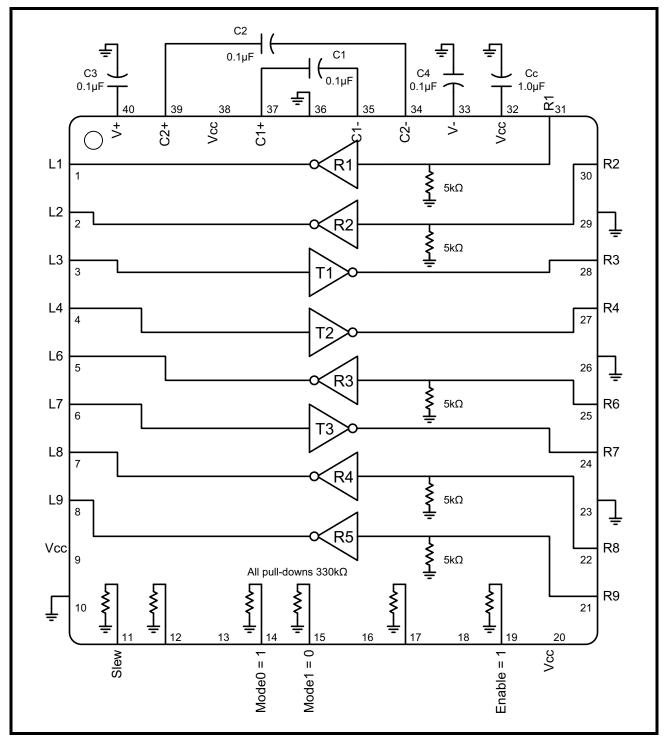


FIGURE 2. MODE 01 - RS-232





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RS-232/RS-485/RS-422 TRANSCEIVER WITH INTERNAL TERMINATION

FIGURE 3. MODE 10 - RS-485 HALF DUPLEX

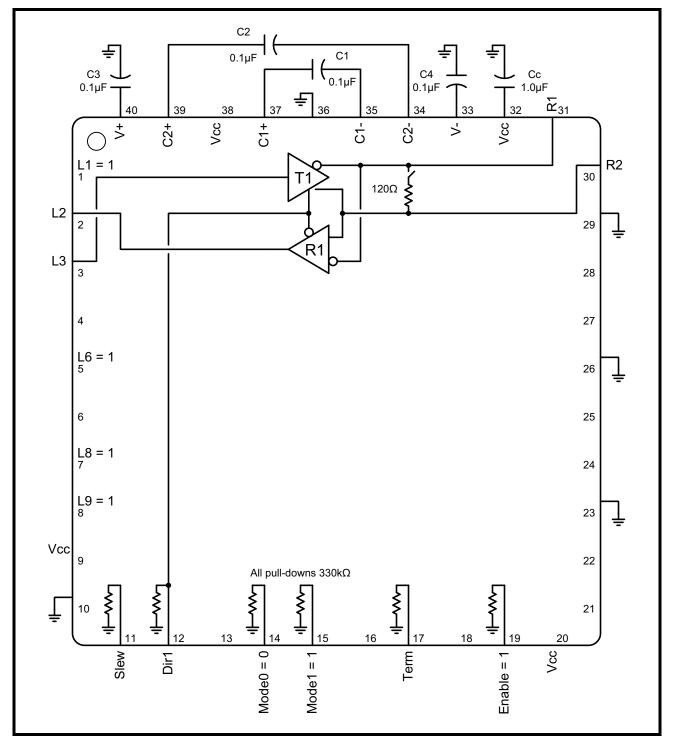
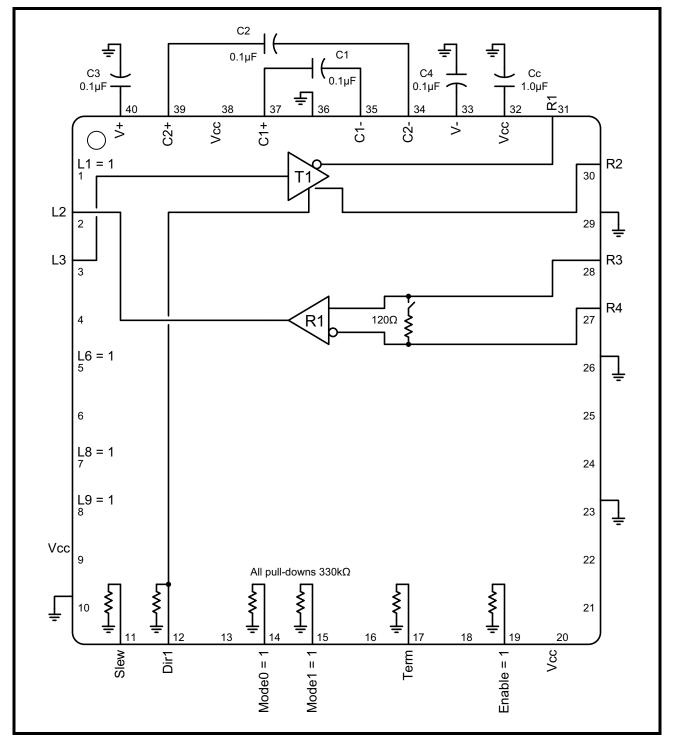


FIGURE 4. MODE 11 - RS-485/422 FULL DUPLEX







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TEST CIRCUITS

FIGURE 5. RS-485/422 RECEIVER TERMINATION RESISTANCE

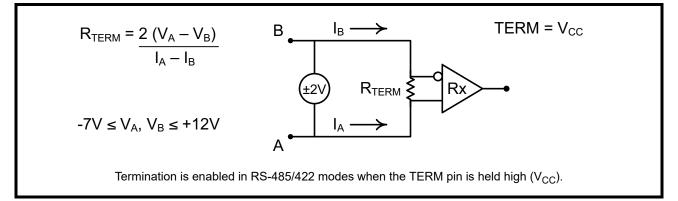


FIGURE 6. RS-485/422 DIFFERENTIAL DRIVER OUTPUT VOLTAGE

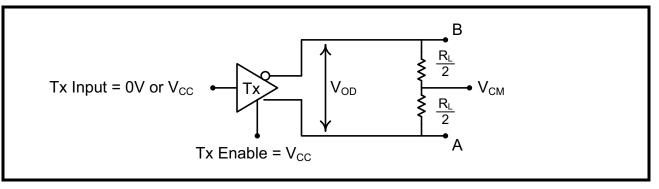
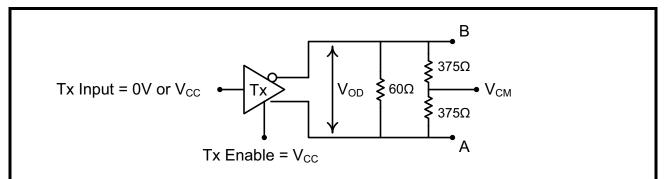
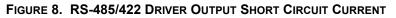


FIGURE 7. RS-485/422 DIFFERENTIAL DRIVER OUTPUT VOLTAGE OVER COMMON MODE





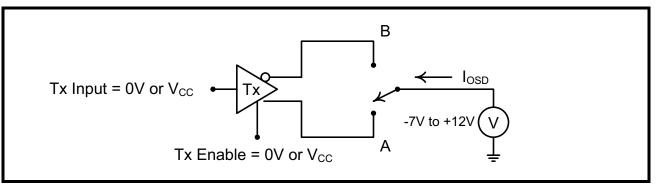




FIGURE 9. RS-232 RECEIVER PROPAGATION DELAY

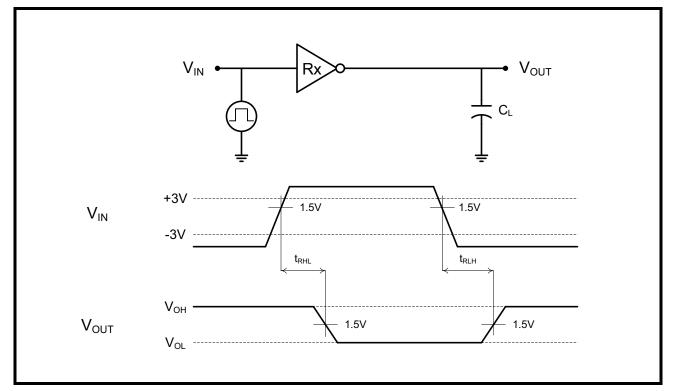
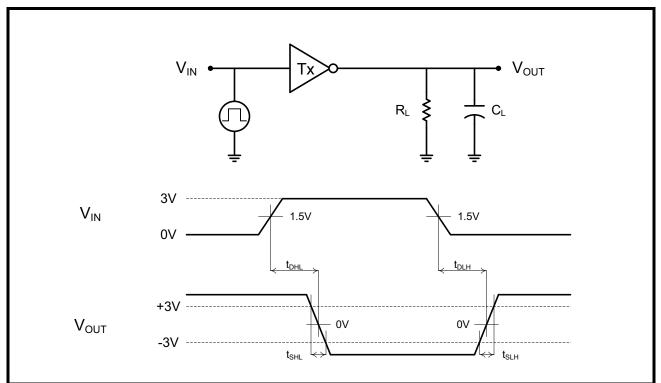


FIGURE 10. RS-232 DRIVER PROPAGATION DELAY





RS-232/RS-485/RS-422 TRANSCEIVER WITH INTERNAL TERMINATION

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FIGURE 11. RS-485/422 RECEIVER PROPAGATION DELAY

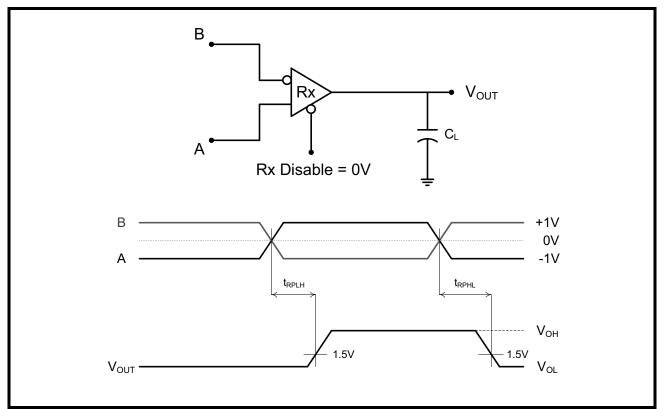
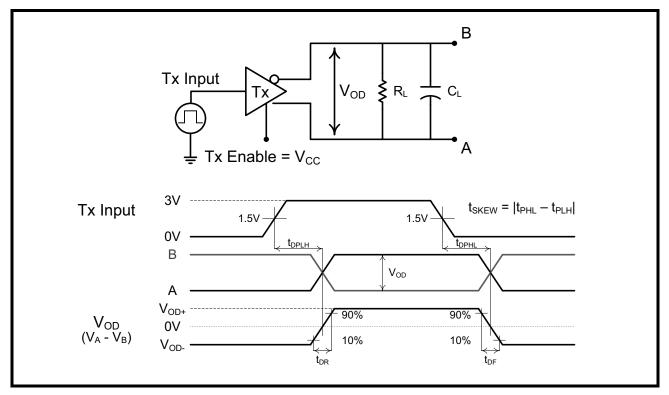
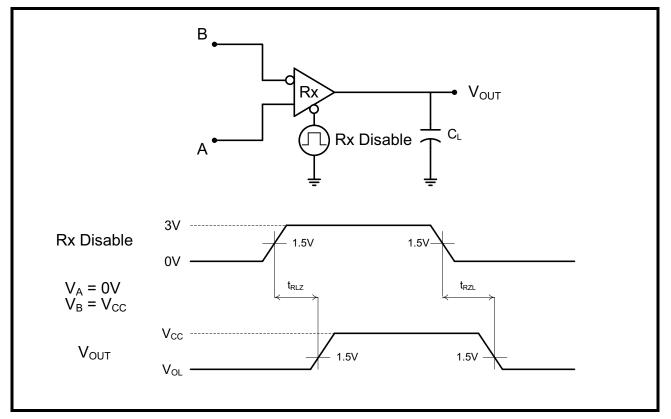


FIGURE 12. RS-485/422 DRIVER PROPAGATION DELAY AND RISE/FALL TIMES



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FIGURE 13. RS-485/422 RECEIVER OUTPUT ENABLE/DISABLE TIMES





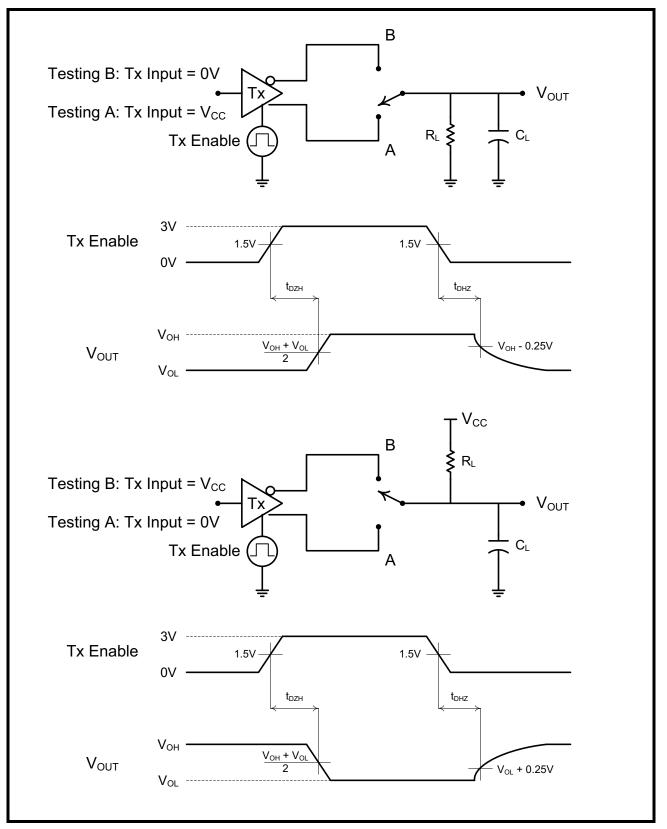


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RS-232/RS-485/RS-422 TRANSCEIVER WITH INTERNAL TERMINATION

FIGURE 14. RS-485/422 DRIVER OUTPUT ENABLE/DISABLE TIMES



PRODUCT SUMMARY

The SP339B is an advanced multiprotocol transceiver supporting RS-232, RS-485, and RS-422 serial standards in a 40 pin QFN package. Integrated cable termination and four configuration modes allow all three protocols to be used interchangeably over a single cable or connector with no additional switching components. The RS-485/422 modes feature one driver and one receiver (1TX/1RX) in both half and full duplex configurations. The RS-232 mode (3TX/5RX) provides full support of all eight signals commonly used with the DB9 RS-232 connector. A dedicated mode is also available for diagnostic loopback testing.

INTERNALLY SWITCHED CABLE TERMINATION

Enabling and disabling the RS-485/422 termination resistor is one of the largest challenges system designers face when sharing a single connector or pair of lines across multiple serial protocols. A termination resistor may be necessary for accurate RS-485/422 communication, but must be removed when the lines are used for RS-232. SP339B provides an elegant solution to this problem by integrating the termination resistor and switching control, and allowing it to be switched in and out of the circuit with a single pin. No external switching components are required.

ENHANCED FAILSAFE

Ordinary RS-485 differential receivers will be in an indeterminate state whenever the data bus is not being actively driven. The enhanced failsafe feature of the SP339B guarantees a logic-high receiver output when the receiver inputs are open, shorted, or terminated but idle/undriven. The enhanced failsafe interprets 0V differential as a logic high with a minimum 50mV noise margin, while maintaining compliance with the EIA/TIA-485 standard of ±200mV. No external biasing resistors are required, further easing the usage of multiple protocols over a single connector.

±15kV ESD PROTECTION

ESD protection structures are incorporated on all pins to protect against electrostatic discharges encountered during handling and assembly. The bus pins (driver outputs and receiver inputs) have extra protection structures, which have been tested up to ± 15 kV without damage. These structures withstand high ESD in all states: normal operation, shutdown and powered down.

ESD protection is be tested in various ways. MaxLinear uses the following methods to qualify the protection structures designed into SP339B:

- ±15kV using the Human Body Model (HBM)
- ± 8kV using IEC 61000-4-2 Contact Discharge
- ± 15kV using IEC 61000-4-2 Air Gap Discharge

The IEC 61000-4-2 standard is more rigorous than HBM, resulting in lower voltage levels compared with HBM for the same level of ESD protection. Because IEC 61000-4-2 specifies a lower series resistance, the peak current is higher than HBM. The SP339B has passed both HBM and IEC 61000-4-2 testing without damage.

DIAGNOSTIC LOOPBACK MODE

The SP339B includes a diagnostic digital loop back mode for system testing as shown in Figure 1. The loopback mode connects the TTL driver inputs to the TTL receiver outputs, bypassing the analog driver and receiver circuitry. The analog/bus pins are internally disconnected in this mode.

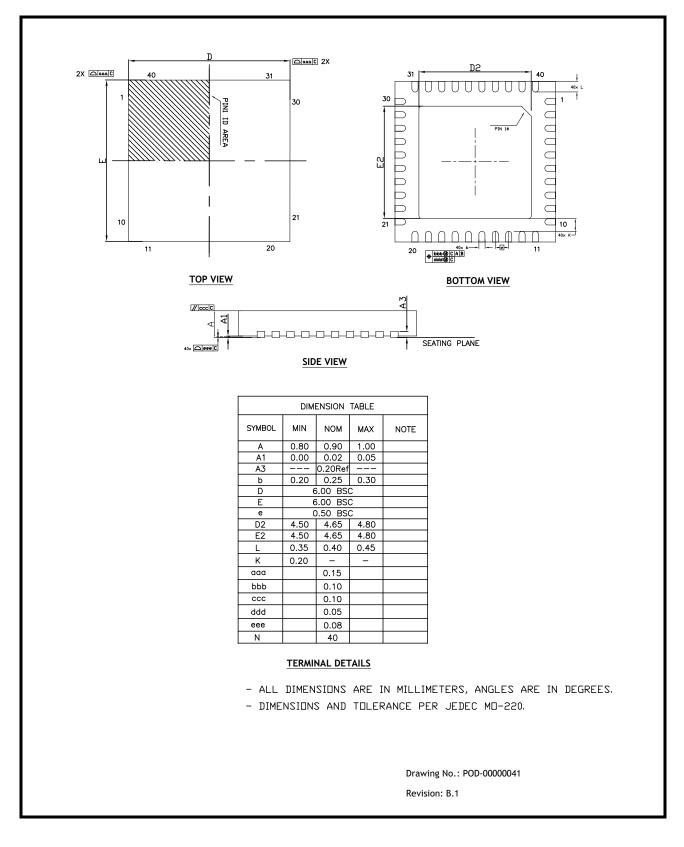




RS-232/RS-485/RS-422 TRANSCEIVER WITH INTERNAL TERMINATION

MECHANICAL DRAWING

FIGURE 15. QFN-40 PACKAGE OUTLINE DRAWING



MAXLINEAR REV. 1.0.1

RECOMMENDED LAND PATTERN AND STENCIL

FIGURE 16. QFN-40 RECOMMENDED PCB LAND PATTERN AND STENCIL

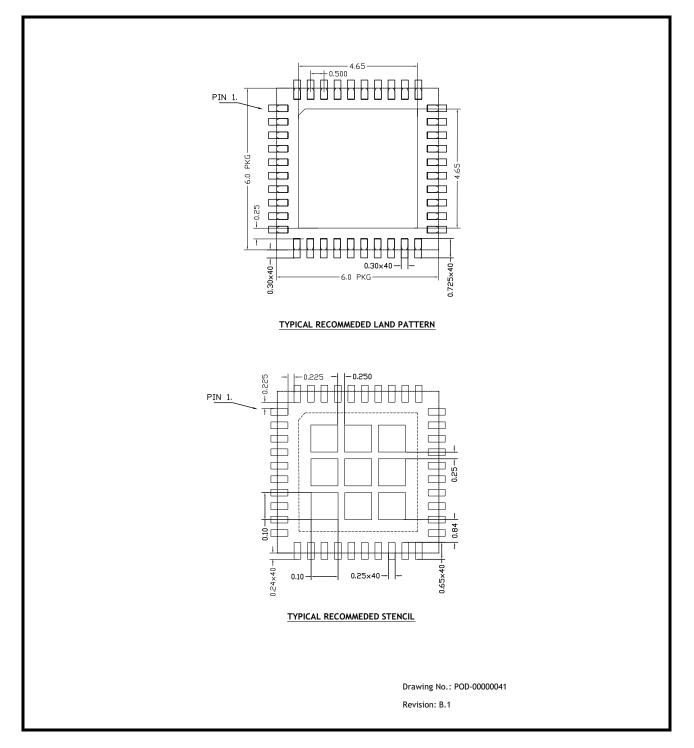
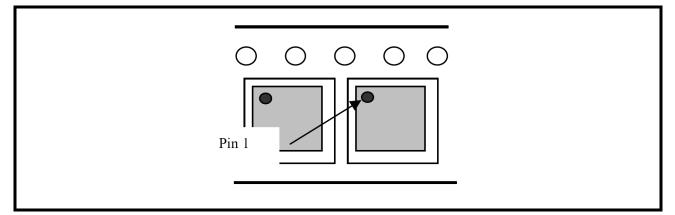




FIGURE 17. PIN 1 ORIENTATION IN TAPE



SP339B

REVISION HISTORY

DATE	REVISION	DESCRIPTION
November 2014	1.0.0	Initial release
February 2018	1.0.1	Update to MaxLinear logo. Update format and Ordering Information. Corrected typo for pin 28, Mode 11 in Pin Description. Moved ESD ratings on pae 2.



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