ESD Protection Diodes

Ultra Small SOT-723 Package

The μ ESD Series is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, portable devices, digital cameras, power supplies and many other portable applications.

Specification Features:

- Small Body Outline Dimensions: 0.047" x 0.032" (1.20 mm x 0.80 mm)
- Low Body Height: 0.020" (0.5 mm)
- Stand-off Voltage: 3.3 V 6.0 V
- Low Leakage
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- IEC61000-4-4 Level 4 EFT Protection
- AEC-Q101 Qualified and PPAP Capable
- These are Pb–Free Devices

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic Epoxy Meets UL 94 V–0 **LEAD FINISH:** 100% Matte Sn (Tin) **MOUNTING POSITION:** Any

QUALIFIED MAX REFLOW TEMPERATURE: $260^\circ\mathrm{C}$

Device Meets MSL 1 Requirements

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
IEC 61000–4–2 (ESD) Air Contact		±30 ±30	kV
IEC 61000-4-4 (EFT)		40	А
ESD Voltage Per Human Body Model Per Machine Model		16 400	kV V
Total Power Dissipation on FR-5 Board (Note 1) @ T _A = 25°C Derate above 25°C Thermal Resistance Junction-to-Ambient	P_D $R_{ hetaJA}$	240 1.9 525	mW mW/°C °C/W
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	ΤL	260	°C

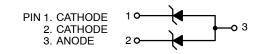
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. $FR-5 = 1.0 \times 0.75 \times 0.62$ in.



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<mark>ت</mark> xx = D

= Device Code = Date Code

M = Date Code

ORDERING INFORMATION

SOT-723

CASE 631AA

STYLE 4

Device	Package	Shipping [†]
UESDxxDT5G	SOT-723	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

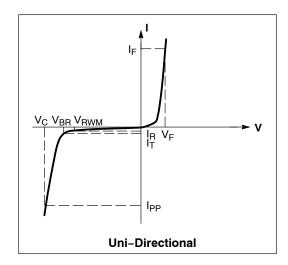
DEVICE MARKING INFORMATION

See specific marking information in the device marking column of the table on page 2 of this data sheet.

ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise noted)

Symbol	Parameter
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
Ι _Τ	Test Current
١ _F	Forward Current
V _F	Forward Voltage @ I _F
P _{pk}	Peak Power Dissipation
Ċ	Max. Capacitance $@V_R = 0$ and f = 1 MHz

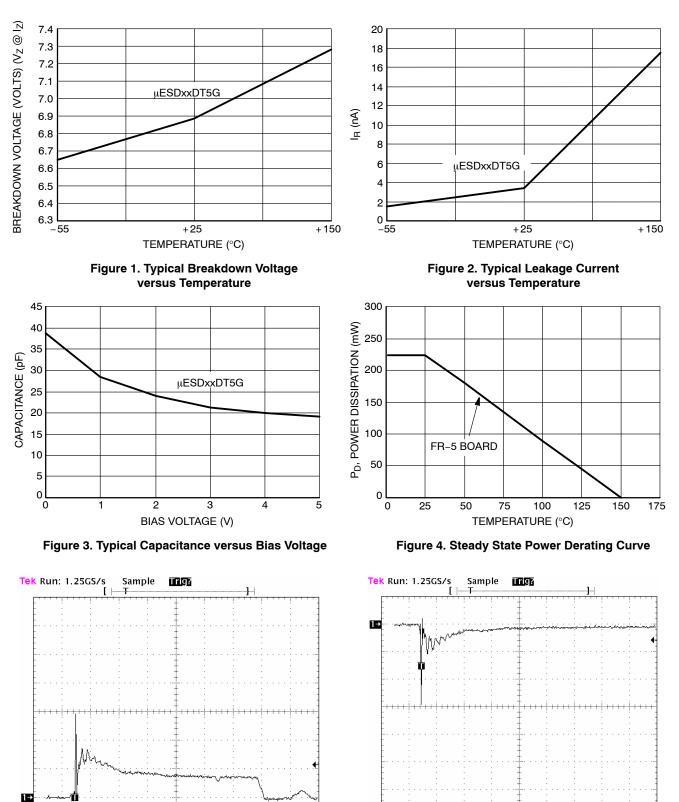


ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted, V_F = 1.1 V Max. @ I_F = 10 mA for all types)

	Device	V _{RWM} (V)	I _R (μΑ) @ V _{RWM}	V _{BR} (V) @ I _T (Note 2)	Ι _Τ	C (pF)
Device*	Marking	Мах	Мах	Min	mA	Тур
UESD3.3DT5G	L0	3.3	1.0	5.0	1.0	47
UESD5.0DT5G	L2	5.0	0.1	6.2	1.0	38
UESD6.0DT5G	L3	6.0	0.1	7.0	1.0	34

*Other voltages available upon request. 2. V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C.

TYPICAL CHARACTERISTICS





M 40.0ns Ch1 J

-5.8 V

11.2 V

M 40.0ns Ch1 J

Figure 5. Positive 8 kV contact per IEC 6100-4-2

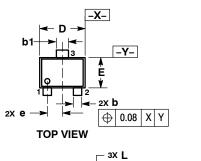
- µESD5.0DT5G

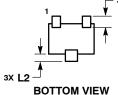
C1 10.0 VΩ

C1% 10.0 VΩ

PACKAGE DIMENSIONS

SOT-723 CASE 631AA **ISSUE D**





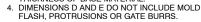
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SIDE VIEW

NOTES DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

3.

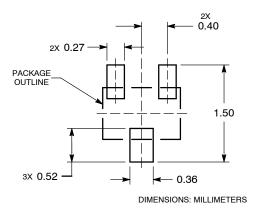
CONTROLLING DIMENSION: MILLIMETERS. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.



	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.45	0.50	0.55
b	0.15	0.21	0.27
b1	0.25	0.31	0.37
С	0.07	0.12	0.17
D	1.15	1.20	1.25
E	0.75	0.80	0.85
е	0.40 BSC		
ΗE	1.15	1.20	1.25
L	0.29 REF		
L2	0.15	0.20	0.25

STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE

RECOMMENDED **SOLDERING FOOTPRINT***



*For additional information on our Pb-Free strategy and soldering

details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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