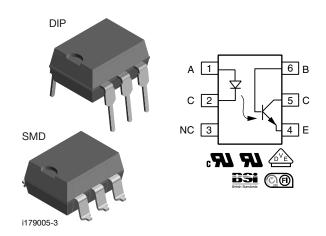




Vishay Semiconductors

# Optocoupler, Phototransistor Output, Low Input Current, With Base Connection



## DESCRIPTION

The IL202, IL203 is optically coupled pairs employing a gallium arsenide infrared LED and a silicon NPN phototransistor. Signal information, including a DC level, can be transmitted by the device while maintaining a high degree of electrical isolation between input and output. The IL202, IL203 can be used to replace relays and transformers in many digital interface applications, as well as analog applications such as CRT modulation.

#### **FEATURES**

- Guaranteed at I<sub>F</sub> = 1.0 mA
- High collector emitter voltage, BV<sub>CFO</sub> = 70 V



- Industry standard DIP package
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912



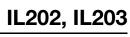


RoHS COMPLIANT

#### **AGENCY APPROVALS**

- UL file no. E52744 system code H, double protection
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-2 (VDE 0884) / DIN EN 60747-5-5 (pending), available with option 1
- BSI: EN 60065:2002, EN 60950-1:2006
- FIMKO

ORDERING INFORMATIO	N					
I L 2 0 PART NUMBER	# - X 0 0 PACKAGE OPTION	# TAPE AND REEL  Option 9  7.62 mm > 0.1 mm				
AGENCY CERTIFIED/PACKAGE	CTR (%)					
UL, cUL, BSI, FIMKO	125 to 250	225 to 450				
DIP-6	-	IL203				
SMD-6, option 9	-	IL203-X009T				
VDE, UL, cUL, BSI, FIMKO	125 to 250	225 to 450				
DIP-6	IL202-X001	-				





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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Peak reverse voltage		V <sub>R</sub>	6.0	V		
Forward continuous current		I <sub>F</sub>	60	mA		
Power dissipation		P <sub>diss</sub>	100	mW		
Derate linearly from 25 °C			1.33	mW/°C		
OUTPUT						
Collector emitter breakdown voltage		BV <sub>CEO</sub>	70	V		
Emitter collector breakdown voltage		BV <sub>ECO</sub>	7.0	V		
Collector base breakdown voltage		BV <sub>CBO</sub>	70	V		
Power dissipation		P <sub>diss</sub>	200	mW		
Derate linearly from 25 °C			2.6	mW/°C		
COUPLER						
Isolation test voltage	t = 1.0 s	V <sub>ISO</sub>	5300	V <sub>RMS</sub>		
Total package dissipation (LED and detector)		P <sub>tot</sub>	250	mW		
Derate linearly from 25 °C			3.3	mW/°C		
Creepage distance			≥ 7.0	mm		
Clearance distance			≥ 7.0	mm		
Storage temperature		T <sub>stg</sub>	-55 to +150	°C		
Operating temperature		T <sub>amb</sub>	-55 to +100	°C		
Lead soldering time	≤ 260 °C		10	S		

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	I <sub>F</sub> = 20 mA	$V_{F}$	-	1.2	1.5	V
	I <sub>F</sub> = 1.0 mA	$V_{F}$	-	1.0	1.2	V
Breakdown voltage	I <sub>R</sub> = 10 μA	$V_{F}$	6.0	20		V
Reverse current	$V_{R} = 6.0 \text{ V}$	I <sub>R</sub>		0.1	10	μΑ
OUTPUT						
DC forward current gain	$V_{CE} = 5.0 \text{ V}, I_{C} = 100 \mu\text{A}$	h <sub>FE</sub>	100	200	-	
Collector emitter breakdown voltage	I <sub>C</sub> = 100 μA	BV <sub>CEO</sub>	70	-	-	V
Emitter collector breakdown voltage	I <sub>E</sub> = 100 μA	BV <sub>ECO</sub>	7.0	10	-	V
Collector base breakdown voltage	I <sub>C</sub> = 10 μA	BV <sub>CBO</sub>	70	90	-	V
Leakage current collector emitter	$V_{CE} = 10 \text{ V}, T_A = 25 ^{\circ}\text{C}$	I <sub>CEO</sub>	-	5.0	50	nA

#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio (collector to base)	$I_F = 10 \text{ mA}, V_{CB} = 10 \text{ V}$		CTR <sub>CB</sub>	15	-	-	%
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 2.0 \text{ mA}$		V <sub>CEsat</sub>	-	-	0.4	V
DC current transfer ratio	I <sub>F</sub> = 10 mA, V <sub>CE</sub> = 10 V	IL202	CTR <sub>DC</sub>	125	200	250	%
		IL203	CTR <sub>DC</sub>	225	300	450	%
	I <sub>F</sub> = 1.0 mA, V <sub>CE</sub> = 10 V	IL202	CTR <sub>DC</sub>	30	-	-	%
		IL203	CTR <sub>DC</sub>	50	-	-	%

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## **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

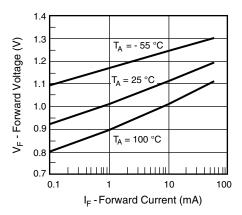


Fig. 1 - Forward Voltage vs. Forward Current

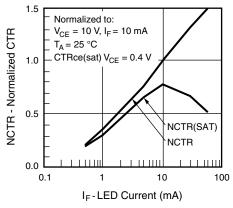


Fig. 2 - Normalized Non-Saturated and Saturated CTR vs. LED Current

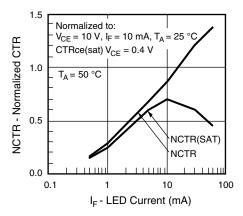


Fig. 3 - Normalized Non-Saturated and Saturated CTR vs. LED Current

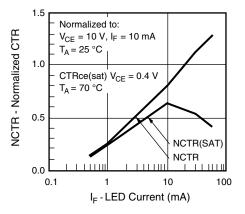


Fig. 4 - Normalized Non-Saturated and Saturated CTR vs. LED Current

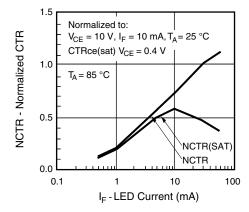


Fig. 5 - Normalized Non-Saturated and Saturated CTR vs. LED Current

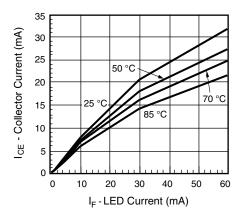


Fig. 6 - Collector Emitter Current vs. Temperature and LED Current



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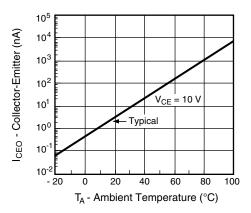


Fig. 7 - Collector Emitter Leakage Current vs.Temperature

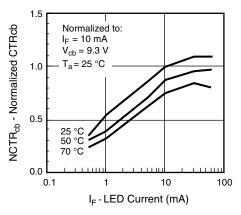


Fig. 8 - Normalized CTR<sub>cb</sub> vs. LED Current and Temperature

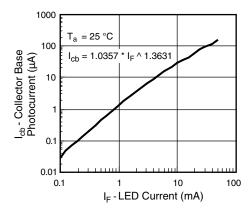


Fig. 9 - Collector Base Photocurrent vs. LED Current

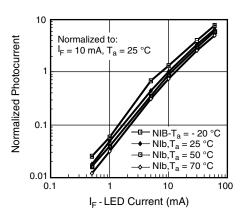


Fig. 10 - Normalized Photocurrent vs.  $I_F$  and Temperature

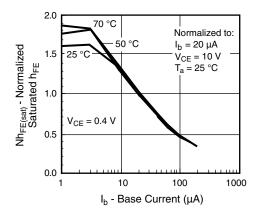


Fig. 11 - Normalized Saturated h<sub>FE</sub> vs. Base Current and Temperature

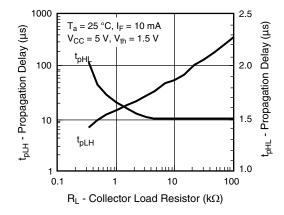


Fig. 12 - Propagation Delay vs. Collector Load Resistor



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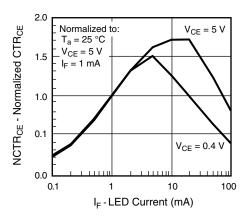


Fig. 13 - Normalized Non-Saturated and Saturated CTR<sub>CE</sub> vs. LED Current

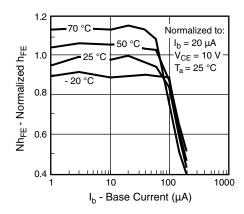
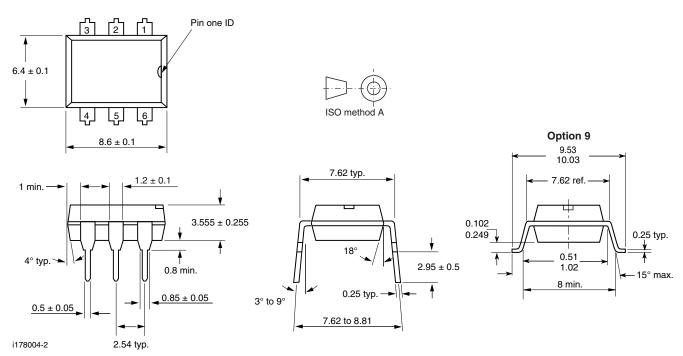


Fig. 14 - Normalized Non-Saturated  $h_{\mbox{\scriptsize FE}}$  vs. Base Current and Temperature

### **PACKAGE DIMENSIONS** in millimeters



## **PACKAGE MARKING** (example)



#### **Notes**

- Only option 1 is reflected in the package marking
- The VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking



## **Legal Disclaimer Notice**

Vishay

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